

# Massiv Parallel Compute Cluster 4



Massiv Parallel Compute Cluster 4th generation

Hardwarecomponents:

## 94 compute nodes

- 2x CPU Intel [E5-2650 v4](#) 2,2 GHz base frequency
- 128 GByte local meemory (RAM)
- local SSD 256 Gbyte capacity
- FDR infiniband interconnect

## 5 highmem nodes

- 2x CPU AMD [AMD EPYC 7601 32-Core](#) 2,2 GHz base frequency
- 2 TeraByte local memory (RAM)
- local SSD 512 Gbyte capacity
- HDR100 infiniband interconnect

## 9 visualization nodes

- 2x CPU Intel
- 512-786 GByte local memory (RAM)
- local SSD 512 Gbyte capacity
- QDR/FDR infiniband interconnect

## 2 login nodes

- 2x CPU Intel
- 512 GByte local memory (RAM)
- local SSD

- QDR/FDR infiniband interconnect

### 3 transfer nodes

- 4x AMD Intel
- 128 GByte local memory (RAM)
- local SSD
- QDR/FDR infiniband interconnect

### GPU nodes

- 2x [Supermicro A+ Server 4124GS-TNR](#): 2x AMD EPYC 7F52 16-Core, 512 GByte RAM, 4x Nvidia A100 PCIe, local NVMe
- 3x [Supermicro A+ Server 2124GQ-NART](#): 2x AMD EPYC 7F52 16-Core, 512 GByte RAM, 4x Nvidia A100 SXM4, local NVMe
- 2x [Gigabyte G492-ZD0](#): 2x AMD EPYC 7742 64-Core, 1024 GByte RAM, 8x Nvidia A100 SXM4, local NVMe

### scratch storage

- 500 Terabyte capacity
- [Beegfs Filesystem](#)
- 4 Objectserver & 1 Metadataserver
- FDR infiniband interconnect
- **no backup!**

### nfs based storage

- 300 Terabyte capacity
- 6 NFS Fileserver
- FDR infiniband interconnect
- with backup!

From:

<https://www1.tu-ilmenau.de/hpcwiki/> - **hpcwiki**

Permanent link:

<https://www1.tu-ilmenau.de/hpcwiki/doku.php?id=mapacc4&rev=1647354734>

Last update: **2022/03/15 15:32**

